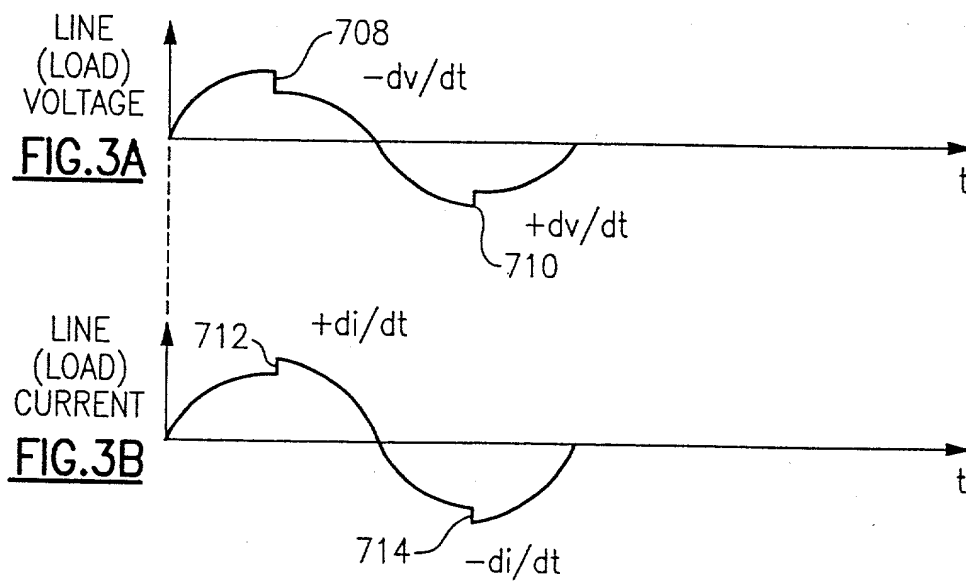
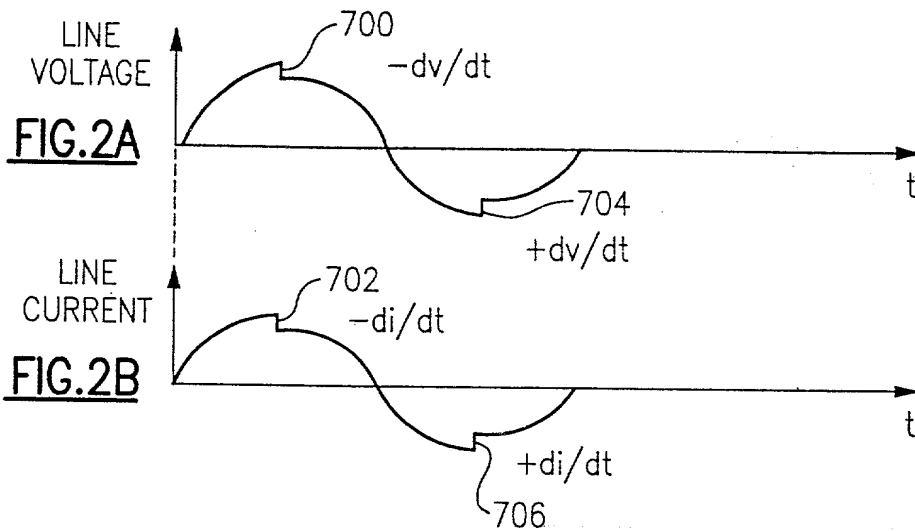
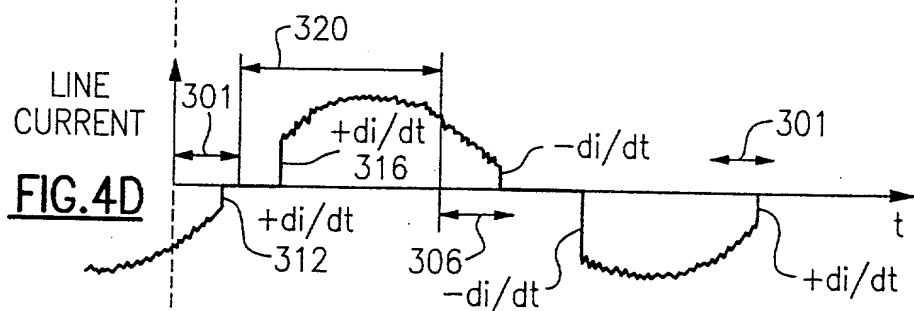
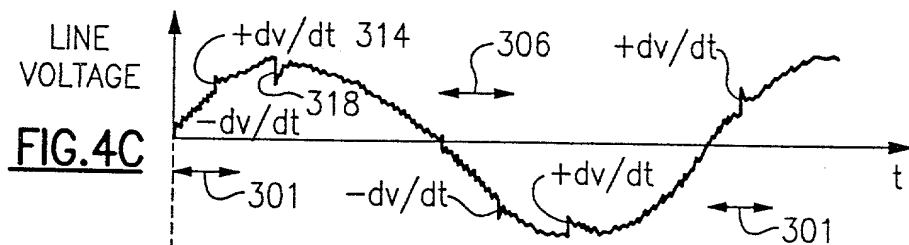
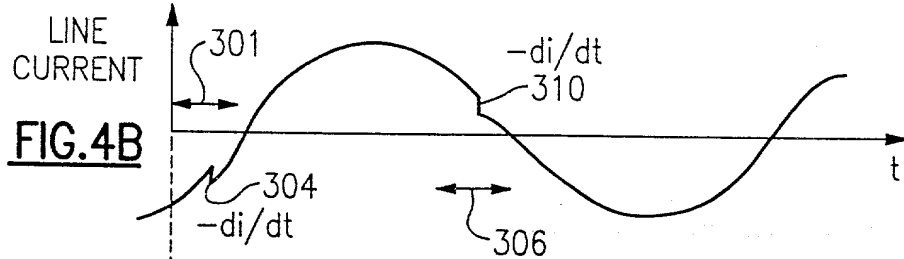
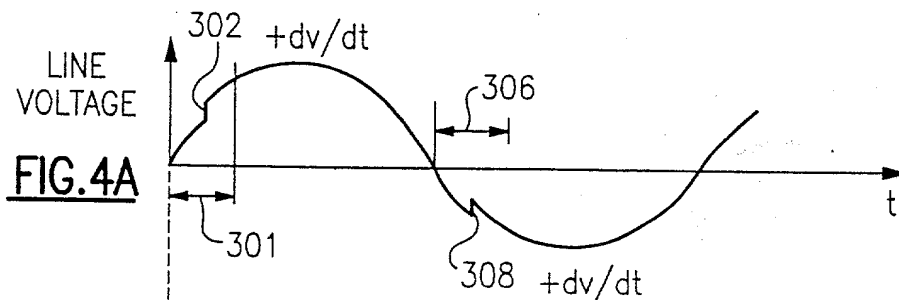
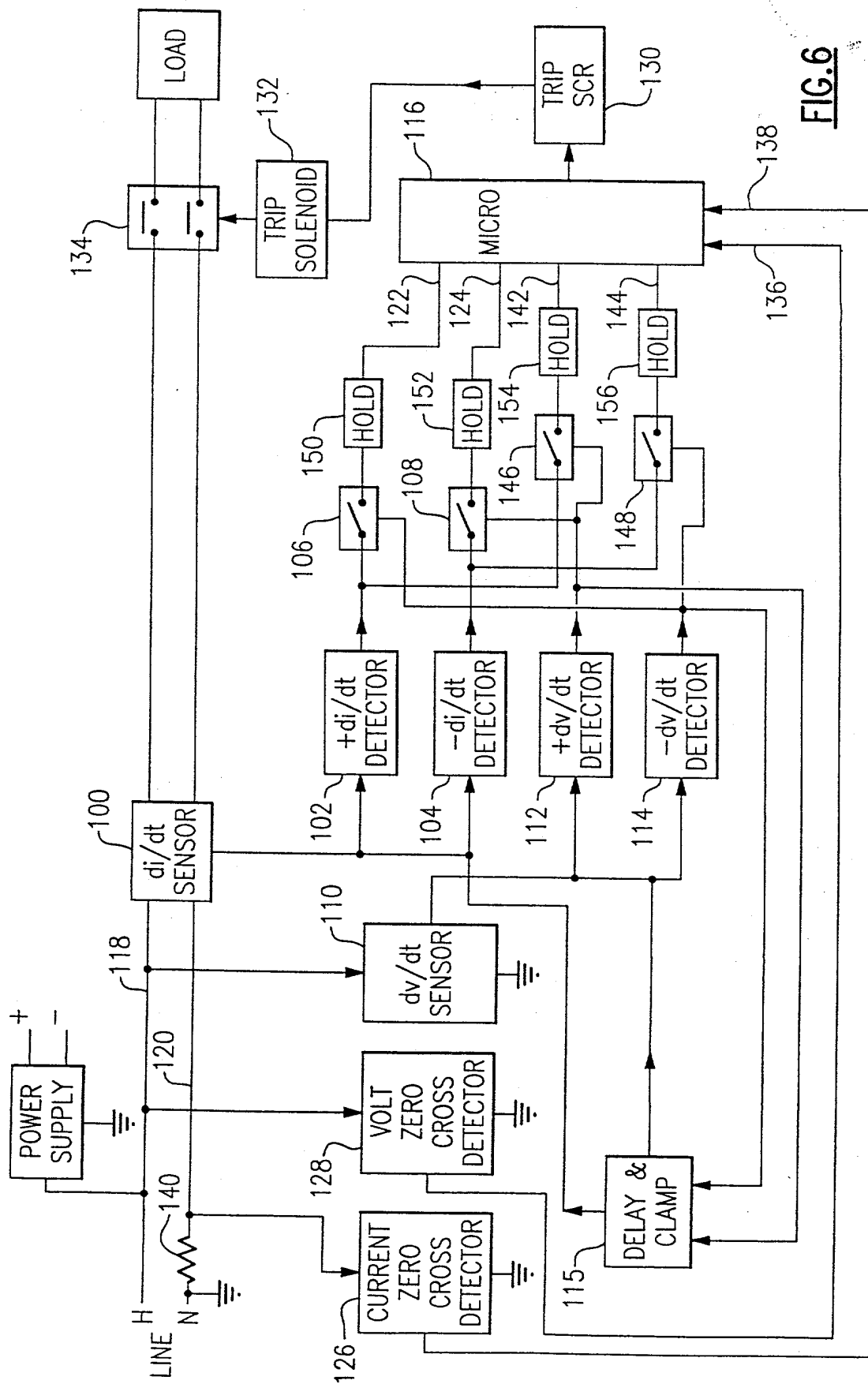


FIG. 1









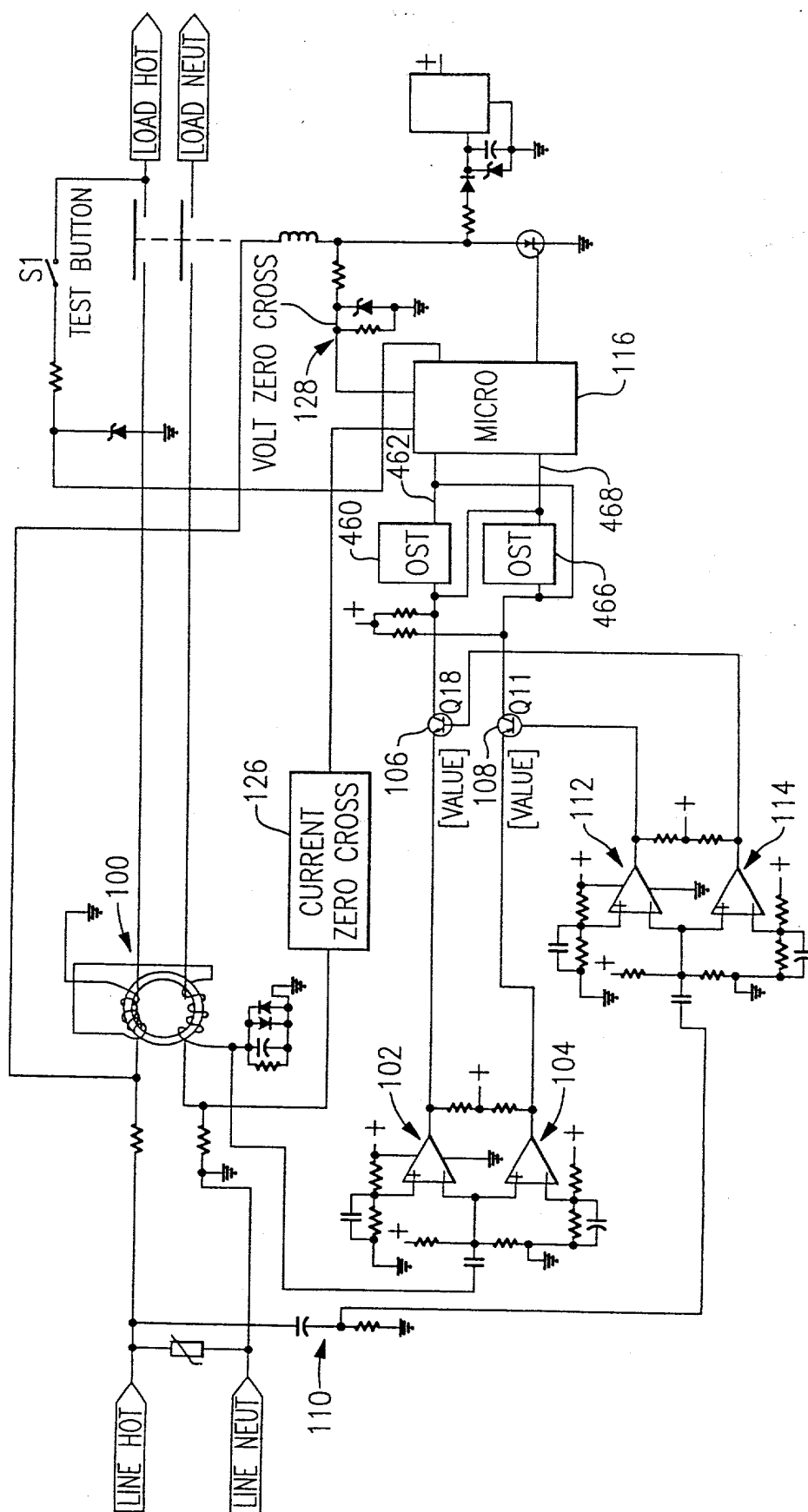


FIG. 7

FIG. 8 is a schematic diagram of a power line monitoring system. The system includes a power line (H, N) connected to a load and a monitoring circuit. The monitoring circuit includes a current sensor (812) and a voltage sensor (814). The current sensor output is connected to an ADC (824) and a MICRO (826). The voltage sensor output is connected to an ADC (834) and a MICRO (826). The MICRO (826) is also connected to a +5V regulator (REG +5V) and a -5V regulator (REG -5V). The system also includes a Zener diode (806) and a Zener diode (808).

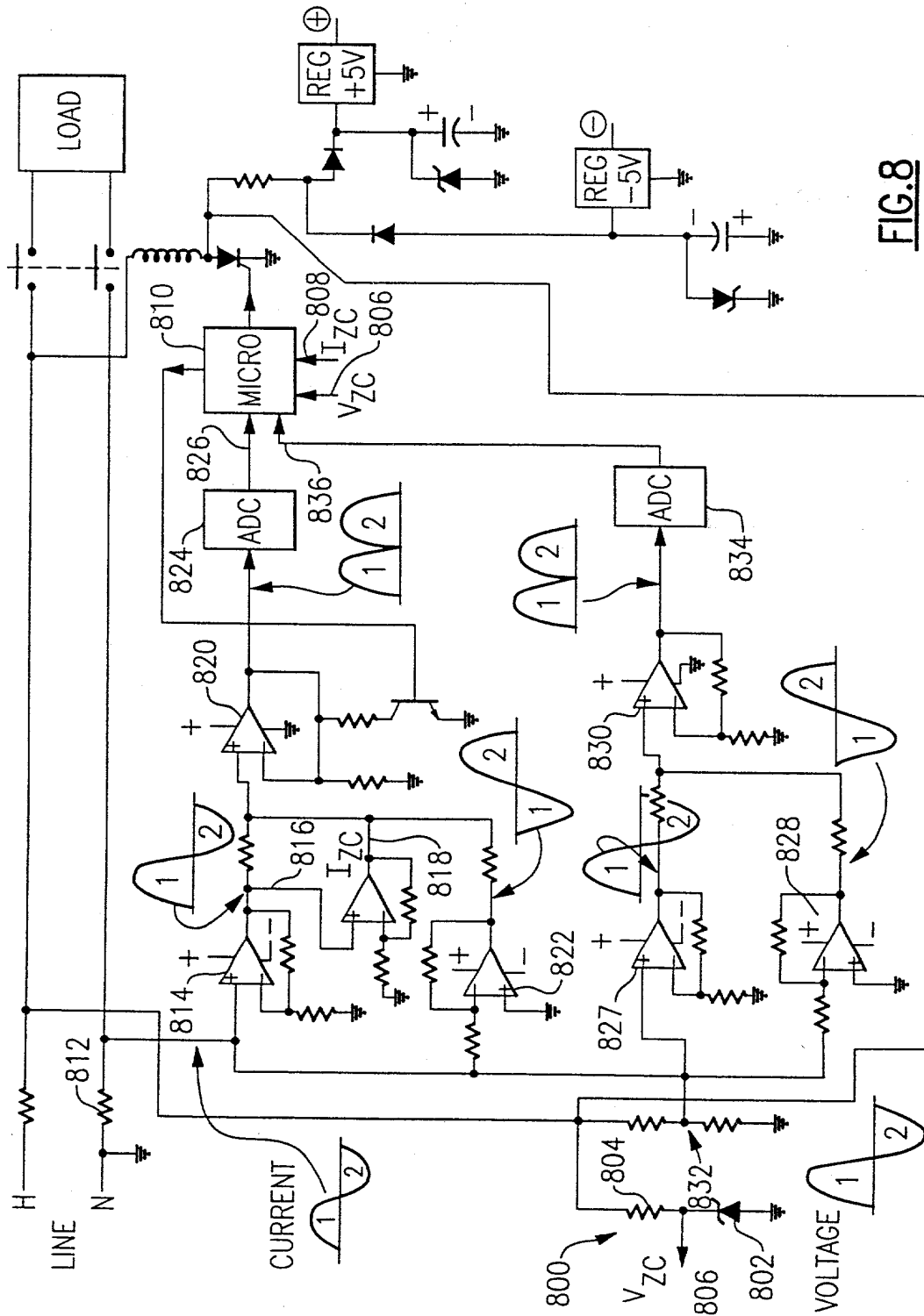


FIG. 8

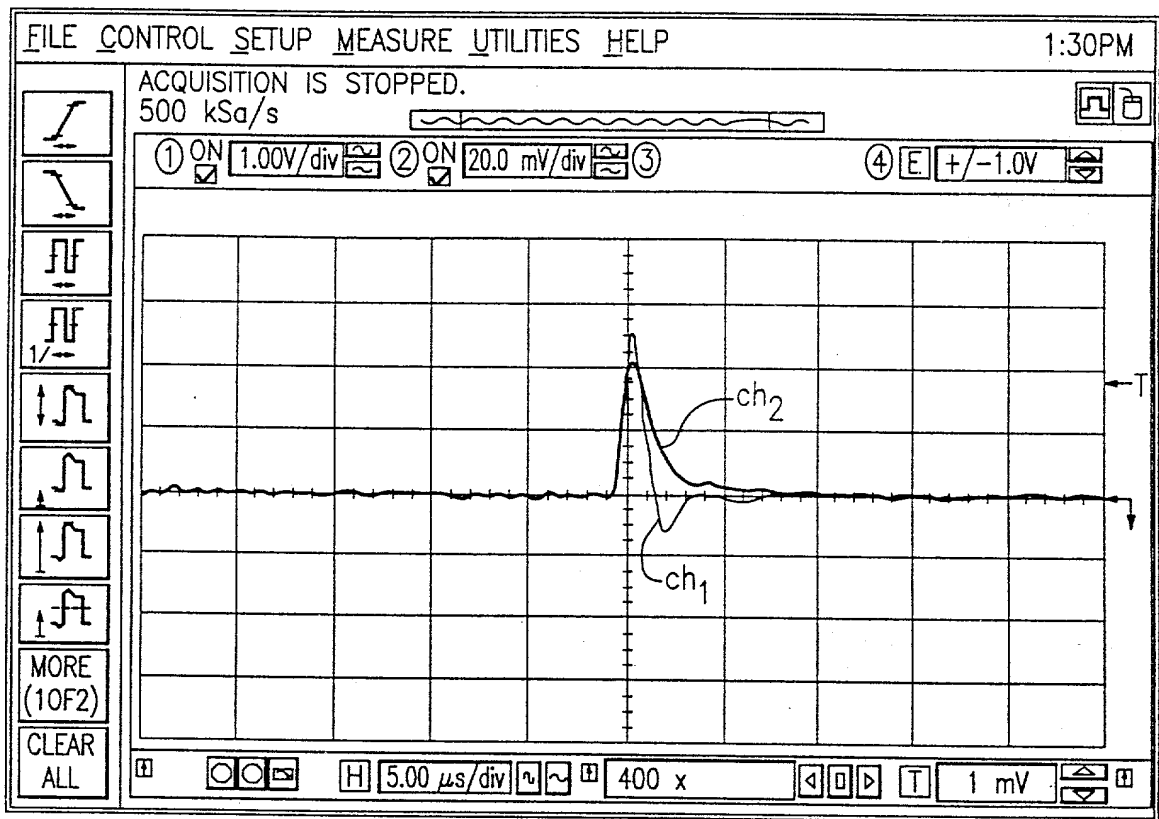


FIG.10

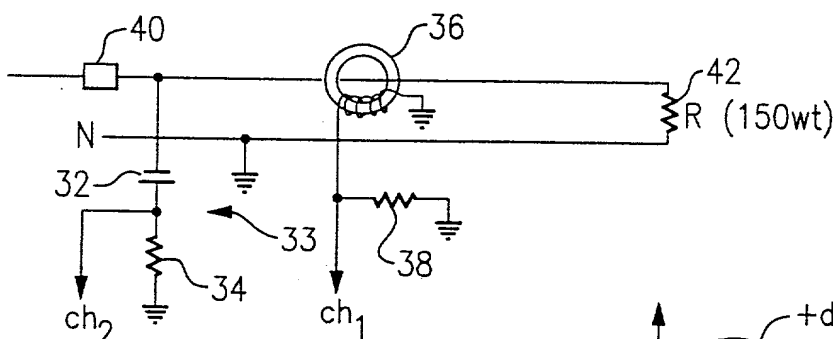


FIG.9

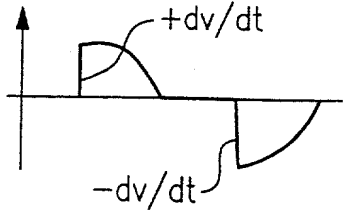


FIG.11

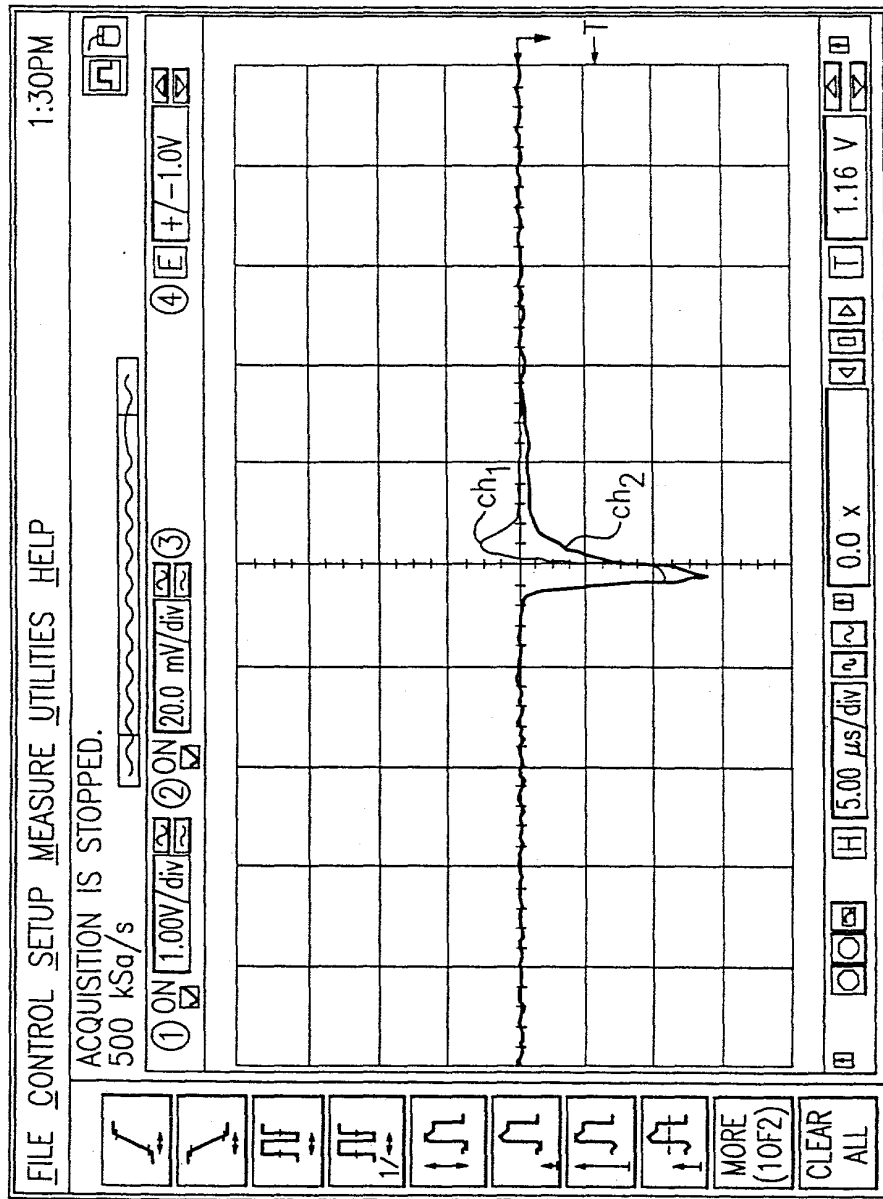


FIG.12

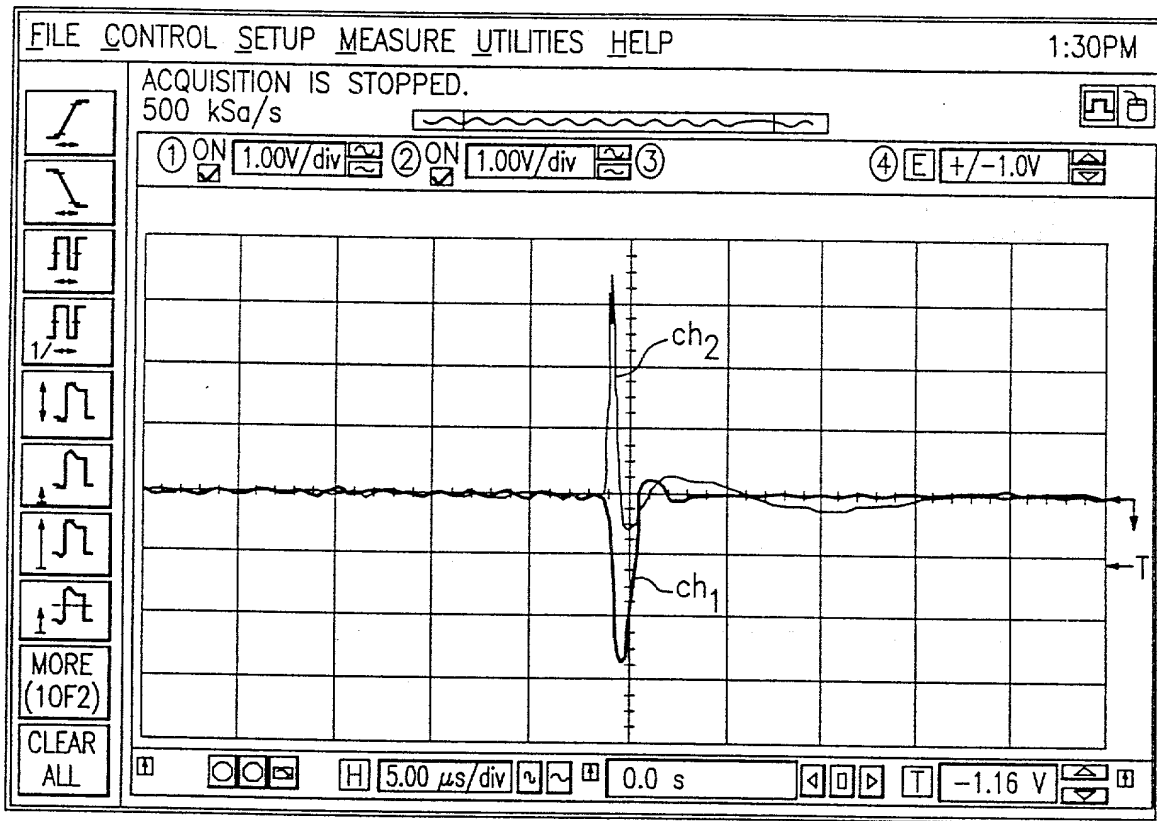


FIG.14

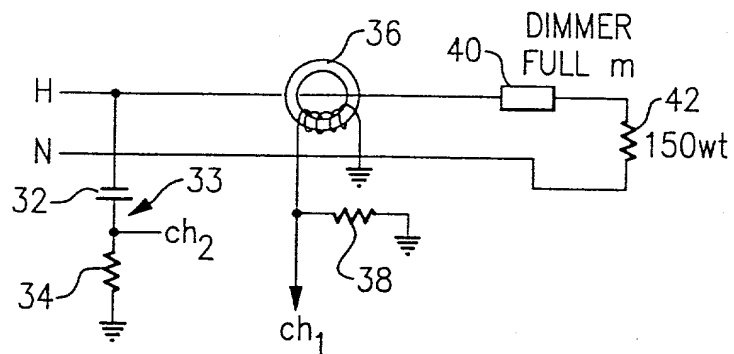


FIG.13

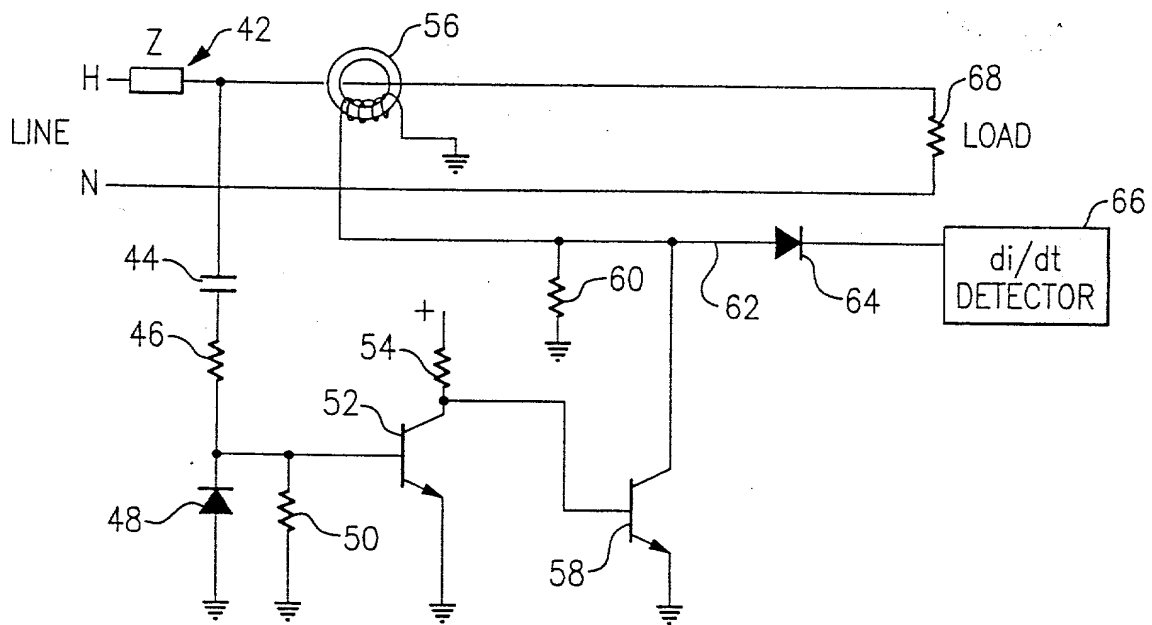


FIG. 15

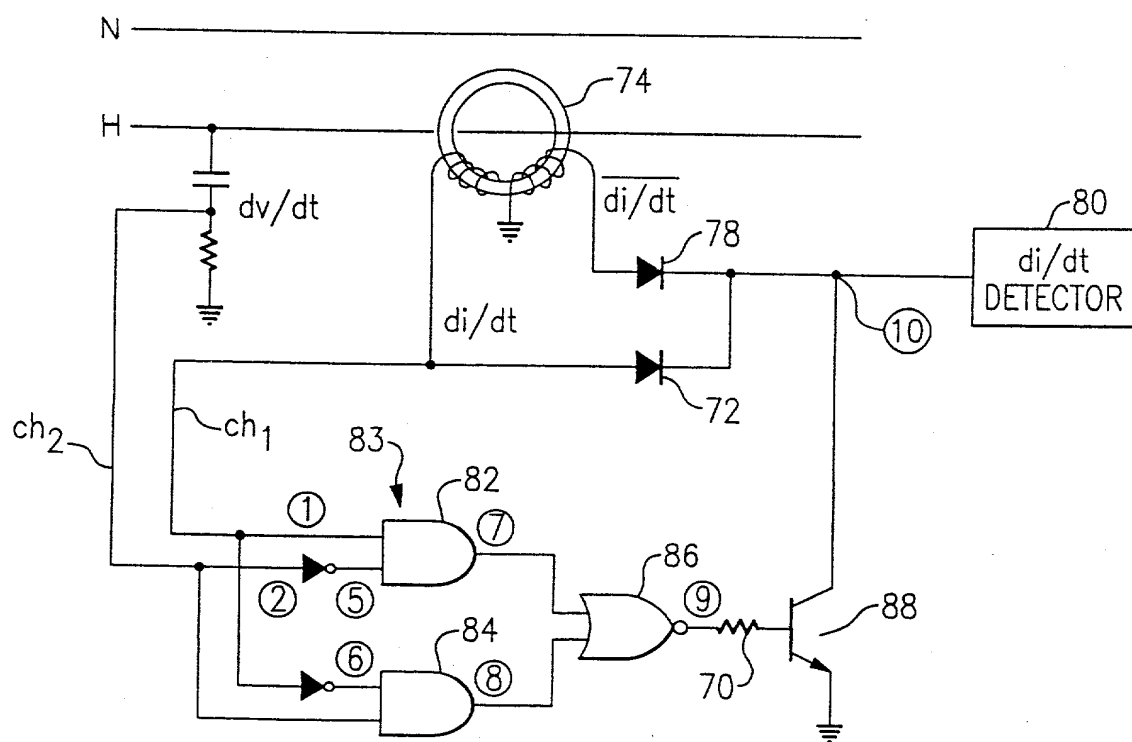


FIG. 16

